IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

KLIESNER ET AL.

Attorney Docket No.
72206

Serial No. NOT YET ASSIGNED

Filing Date: HEREWITH

Confirmation No. NOT YET ASSIGNED

For: DIGITAL CLOCK RECOVERY CIRCUIT)

EMPLOYING FIXED CLOCK

OSCILLATOR DRIVING FRACTIONAL

DELAY LINE

Attorney Docket No.
72206

CITATION UNDER 37 CFR §1.97

Director, U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

27975

PATENT TRADEMARK OFFICE ·

Respectfully submitted,

CHARLES E. WANDS Reg. No. 25,649

Telephone: (321) 725-4760

In re Patent Application of:

KLIESNER ET AL.

Serial No. NOT YET ASSIGNED

Filed: HEREWITH

CERTIFICATE OF MAILING

flator Leiger

FORM PTO-1449 LIST OF PATENTS AND APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTORNEY DOCKET NO.: 72206

SERIAL NO.: NOT YET ASSIGNED

FILING DATE: HEREWITH

APPLICANT:

KLIESNER ET AL.

GROUP: NOT YET ASSIGNED

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS							
EXAMINER INITIALS			CLASS	SUB CLASS	FILING IF APPROPRIATE		
	AA						
	AB			 			
	AC		· · ·				
	AD						
	AE						
	AF						
	AG						
	АН						
	AI						

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION Yes No
	АМ						
	AN						
	OA						* *** *** *** *** *** *** *** *** ***
	AP						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)					
AQ	Lars Erup, Floyd M. Gardner, And Robert A. Harris. Interpolation in digital modems - Part II: Implementation and performance. IEEE Transactions on Communication, 41(6):998-1008, June 1993				
AR	Timo I. Laasko, Vesa Valimaki, Matti Karjalainen, and Unto K. Laine. Splitting the unit delay. IEEE Signal Processing Magazine, Pages 30-60, January 1996.				
AS	Heinrich Meyer, Marc Moeneclaey, and Stefan A. Fechtel. Digital Communication Receivers: synchronization, channel estimation and signal processing. John Wiley & Sons, 1998.				

EXAMINER:	DATE CONSIDERED:				
<u> </u>					

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; * Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, DC 20231

In re Application of:		tion of: KLIESNER ET AL.
Serial	No.:	NOT YET ASSIGNED
Filed:		HEREWITH
For:		DIGITAL CLOCK RECOVERY CIRCUIT EMPLOYING FIXED CLOCK OSCILLATOR DRIVING FRACTIONAL DELAY LINE
Sir:		
		herewith is an INFORMATION DISCLOSURE STATEMENT in the fied application.
1.	[X]	This IDS is submitted under 37 C.F.R. ' 1.97. No fee is required.
2.	[]	This IDS is submitted under 37 C.F.R. ' 1.97(c). Enclosed is a check in the amount of \$_180.00 .
3.	[.]	This IDS is submitted under 37 C.F.R. ' 1.97(c) and (e). No fee is required.
4.	[]	This IDS is submitted under 37 C.F.R. '1.97(d) and (e). Enclosed is a check in the amount of \$130.00 to cover the petition fee.
5.	[X]	The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
Date:	July	15, 2003 Charles E. Wands

Telephone: (321) 725-4760